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Low latency network-on-chip router

The allocator is the most challenging module to design since the overall NoC router performance and area overhead will be dominated by this module.

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both inside the router's pipeline and at the network level. G. Dimitrakopoulos, Microarchitecture of Network-on-Chip Routers: A designer's perspective.

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This book provides a unified overview of network-on-chip router micro-architecture, the corresponding design opportunities and challenges, and existing solutions to